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Panel: HARDWARE/SOFTWARE CO-VERIFICATION

... between hardware and software so tools can work across **multiple** applications:

... There is no single hardware-software co-**simulation** problem and as a ...

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... Panelists: **Brian Bailey**, Kurt Keutzer, Amr Mohsen, Richard Moseley, ...

38.1 Fault **Simulation** under the **Multiple** Observation Time Approach using ...

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... In RTL is a clock a single event or does it have **multiple** phases that can be

used. ... A-12 Formal invite to Chris and Joe to regsiter. **Brian Bailey** ...

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... Undefined is for **simulation**. Don't care is for synthesis – a directive. ...

Contact Steve Shultz about contributing to this group (**Brian Bailey**) ...

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... component-based modeling, **simulation** and design of multi-core SoC ... Chair:

Brian Bailey, Mentor, UK, Co Chair: Wolfgang Ecker, Infineon, Germany ...

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... A language for hybridsystem engineering modeling and **simulation**. ... **Brian Bailey**.

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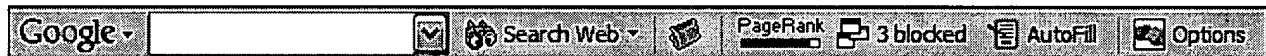
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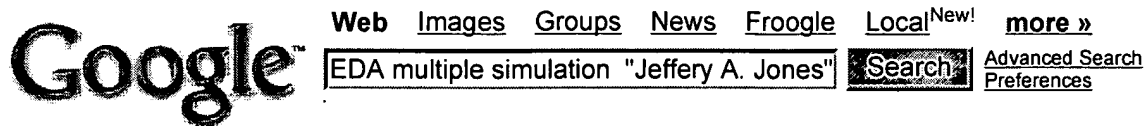
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